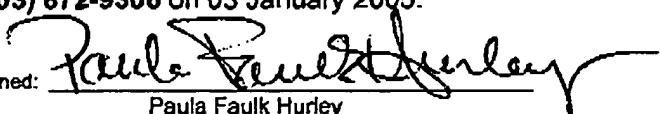


Application No. 10/632,553  
Attorney Docket: CPAC 1017-6 D4CERTIFICATE OF FACSIMILE TRANSMISSION

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 Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 at  
 (703) 872-9306 on 03 January 2005.

Signed:


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***IN THE UNITED STATES PATENT AND TRADEMARK OFFICE***

In re application of: Marcos Karnezos Application No.: 10/632,553 Filed: 02 August 2003 Title: Semiconductor Multi-Package Module Having Package Stacked Over Die-Down Flip Chip Ball Grid Array Package And Having Wire Bond Interconnect Between Stacked Packages	Attorney Docket No.: CPAC 1017-6 D4 Examiner: Unassigned Group: 2812 Confirmation No.: 2573 Customer No. 22470
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**INFORMATION DISCLOSURE STATEMENT**

Mail Stop Amendment  
 Commissioner for Patents  
 P.O. Box 1450  
 Alexandria, VA 22313-1450

Sir:

It is requested that the information identified in this statement be considered by the Examiner and made of record in the above-identified application. This statement is not intended to represent that a search has been made or that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 C.F.R. 1.56.

Enclosed with this statement is a Form PTO/SB/08A. The Examiner is requested to initial the form and return it to the undersigned in accordance with M.P.E.P. 609.

Also enclosed with this statement is a copy of each cited document as required by 37 C.F.R. 1.98. However, since this application was filed after June 30, 2003, copies of U. S. Patents and U. S. Patent Application Publications are not enclosed. (See O. G. Notice of August 5, 2003.)

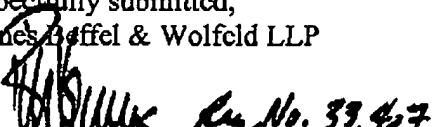
This statement should be considered under 37 C.F.R. 1.97(b) because it is being filed

Application No. 10/632,553  
Attorney Docket: CPAC 1017-6 D4

before the mailing date of the first Office Action on the merits.

**Fee Authorization.** The Commissioner is hereby authorized to charge any additional fees or credit any overpayment associated with this communication to Deposit Account No. 50-0869 (CPAC 1017-6 D4). A duplicate copy of this authorization is enclosed.

Respectfully submitted,  
Haynes Baffel & Wolfeld LLP

By:  Reg. No. 33,407

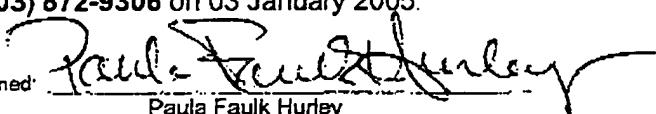
Bill Kennedy, Reg. No. 33,407

Date: 03 January 2005  
Bill Kennedy  
Haynes Baffel & Wolfeld LLP  
P.O. Box 366  
Half Moon Bay, CA 94019  
Ph. (650) 712-0340  
Fax (650) 712-0263  
[bkennedy@hmbay.com](mailto:bkennedy@hmbay.com)  
BK:pjh

Application No. 10/632,553  
Attorney Docket: CPAC 1017-6 D4CERTIFICATE OF FACSIMILE TRANSMISSION

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Signed:


 Paula Faulk Hurley
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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: Marcos Karnezos	Attorney Docket No.: CPAC 1017-6 D4
Application No.: 10/632,553	Examiner: Unassigned
Filed: 02 August 2003	Group: 2812
Title: Semiconductor Multi-Package Module Having Package Stacked Over Die-Down Flip Chip Ball Grid Array Package And Having Wire Bond Interconnect Between Stacked Packages	Confirmation No.: 2573
	Customer No. 22470

**INFORMATION DISCLOSURE STATEMENT**

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Enclosed with this statement is a Form PTO/SB/08A. The Examiner is requested to initial the form and return it to the undersigned in accordance with M.P.E.P. 609.

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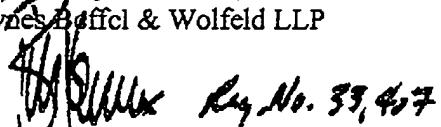
This statement should be considered under 37 C.F.R. 1.97(b) because it is being filed

Application No. 10/632,553  
Attorney Docket: CPAC 1017-6 D4

before the mailing date of the first Office Action on the merits.

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Respectfully submitted,  
Haynes Belfel & Wolfeld LLP

By:   
Bill Kennedy, Reg. No. 33,407

Date: 03 January 2005

Bill Kennedy  
Haynes Belfel & Wolfeld LLP  
P.O. Box 366  
Half Moon Bay, CA 94019  
Ph. (650) 712-0340  
Fax (650) 712-0263  
[bkennedy@hmabay.com](mailto:bkennedy@hmabay.com)  
BK:pfh

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STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet

1

of

1

Complete if Known

Application Number	10/632,553
Filing Date	08/02/03
First Named Inventor	Karnezos
Group Art Unit	2812
Examiner Name	Unassigned

Attorney Docket Number CPAC 1017-6 D4

## U.S. PATENT DOCUMENTS

Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	U.S. Patent Document Number	Kind Code <sup>2</sup> (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	A1	5,495,398		Takiar et al.	02-27-1996	
	A2	6,472,741		Chen et al.	10-29-2002	
	A3	6,545,365		Kondo et al.	04-08-2003	
	A4	6,593,647		Ichikawa	07-15-2003	
	A5					
	A6					
	A7					
	A8					
	A9					
	A10					
	A11					
	A12					
	A13					
	A14					
	A15					
	A16					
	A17					
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	A19					
	A20					

## FOREIGN PATENT DOCUMENTS

Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Foreign Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)			
	B1						
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<sup>1</sup> Unique citation designation number. <sup>2</sup> See attached Kinds of U.S. Patent Documents. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbol as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

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